# Resistance of Feedback Amplifiers: A Novel Representation

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Abstract—We propose a representation and a related methodology for evaluating the exact input and output resistances of feedback amplifiers. Our approach is based on the generalization of the Rosenstark theorem. Indeed, it requires the computation of two resistances (direct and asymptotic) each one evaluated in one of the two ideal and extreme conditions of the return ratio (zero or infinity). Due to these characteristics, the representation allows one to understand what happens to a feedback amplifier resistance in the case of absence of feedback or in the ideal case of infinite feedback.

*Index Terms*—Circuit analysis, circuit modeling, circuit theory, feedback amplifiers, feedback circuits.

# I. INTRODUCTION

**F** EEDBACK has the property to change (increase or decrease) the input/output resistances (impedances) of openloop amplifiers [1]–[11]. And this property is widely used in the design of electronic and microelectronics circuits [12]–[14].

As in the case of the evaluation of feedback amplifier transfer functions, there are different methods for calculating the exact (such as [5] and [15], [16]), or approximated (as described in [3], [4] and [11]) input/output resistances of closed-loop amplifiers. Among the approaches that lead to exact results, the most immediate one is analyzing the circuit directly by writing the Kirchhoff equations of the small-signal circuit. However, this approach is computationally tedious, and does not allow one to understand deeply how the feedback modifies the properties of the original open-loop circuit.

Another exact approach models both the amplifier and the feedback network with their proper two-port models [3]. However, due to the non-unidirectional nature of real circuits, the two-port network which models each block has to be chosen judiciously and, for exact results, the analysis becomes extremely elaborate.

An interesting method to evaluate exact input and output resistances of feedback amplifier was reported in [15] by Cherry. This exact method is an extension of an approximated approach previously presented in [2]. However, the method needs to identify the feedback network, classify the feedback amplifier in one

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of the four classical topologies [3], and properly break the loop to evaluate the loop gain. Thus, this methodology needs an *a priori* knowledge and a circuit expertise.

An efficient approach for evaluating exact input or output resistances of feedback amplifiers was introduced by Blackman in 1943 [16]. Subsequently, the technique, also included in the textbooks [1] and [3], was revisited by Rosenstark [17], and was developed independently by Choma through signal flow analysis [18]. Hence, the Blackman theorem represents a relevant methodology for evaluating input and output resistances of feedback amplifiers even for those who make use of the Rosenstark or the Choma method.

It is worth noting that, to the best of the authors' knowledge, while the analytical representation of the Choma method (which applies for the determination of closed-loop transfer functions) is a clear generalization of the Blackman theorem, a Rosenstark-like representation of resistances of feedback amplifiers has never been proposed. Indeed, even Rosenstark in its original manuscript suggested the use of the Blackman theorem to evaluate input and output resistances of feedback amplifiers [17]. However, as we shall discuss in the next section, despite its advantages that justify its remarkable fame, the Blackman theorem has some drawbacks, too.

In this brief, we propose an alternative representation and a related methodology for evaluating the exact input and output resistances of feedback amplifiers. The proposed approach comes out from a generalization of the Rosenstark representation, and provides some interesting features.

# II. BLACKMAN THEOREM

Without any loss of generality, let us consider the block schema of the amplifier in Fig. 1 and assume we want to evaluate the resistance  $r_x$  at gate a-b. Naming the amplifier return ratio as  $T(R_x)$ , where the dependence on the external resistor  $R_x$  is explicitly shown, the Blackman theorem states that [8]–[10]

$$r_x = r_{x,ol} \frac{1 + T(0)}{1 + T(\infty)}.$$
(1)

In relationship (1),  $r_{x,ol}$  is the resistance at gate a-b in openloop configuration (more in general, when the return ratio is zero); T(0) and  $T(\infty)$  are the return ratios evaluated when gate a-b is short-circuited ( $R_x = 0$ ) or left open ( $R_x \to \infty$ ), respectively.

A drawback of the Blackman representation is due to the fact that, differently from the Rosenstark representation for the amplifier transfer function, it does not give any explicit information

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Fig. 1. Block schema of a feedback amplifier.



Fig. 2. General signal flow graph of a feedback circuit.

about the ideal and asymptotic value of  $r_x$ , (i.e., when  $T(R_x) \rightarrow \infty$ ), which becomes an important and practical parameter in many applications. Moreover, the Blackman theorem requires two different return ratios. Thus, even if T(0) and  $T(\infty)$  can be evaluated from a more general relationship  $T(R_x)$ , the evaluation of  $r_x$  is a tedious task.

## III. PROPOSED REPRESENTATION AND METHODOLOGY

## A. Introduction to Flow Graph Analysis

Consider the linear circuit represented by the signal flow graph shown in Fig. 2 [8], [11]. Variables  $x_s$  and  $x_o$  represent the input and output signals, moreover, two other generic variables,  $x_i$  and  $x_j$ , linked together through the control (or critical) parameter P, are explicitly shown. Parameters  $a_{ij}$  are the weight branches. Variables  $x_i, x_j$  and the control parameter, P, model a controlled generator, or the relation between voltage and current across two nodes of the circuit.

Inspecting Fig. 2, we note that term  $a_{11}$  is the transfer function between the input and the output, setting the control parameter, P, to zero; term  $a_{12}$  is the transfer function between the output and the controlled variable,  $x_j$ , setting the input source,  $x_s$ , to zero; term  $a_{21}$  represents the transfer function between the source variable and the inner variable,  $x_i$ , when the controlled variable,  $x_j$ , is set to zero (i.e., when the control parameter, P, is set to zero); term  $a_{22}$  gives the relation between the independent and the controlled inner variables setting control parameter, P, and input variable,  $x_s$ , to zero.

Writing the variables  $x_o$  and  $x_i$  in terms of  $x_s$  and  $x_j$ , we get

$$x_0 = a_{11}x_s + a_{12}x_j \tag{2a}$$

$$x_i = a_{21}x_s + a_{22}x_j. \tag{2b}$$

Then, exploiting the relationship  $x_j = Px_i$  and solving (2b) for  $x_i$ , yields

$$x_i = \frac{a_{21}}{1 - Pa_{22}} x_s. \tag{3}$$

By substituting (3) into (2a), the following relationship holds:

$$X_F = \frac{x_o}{x_s} = a_{11} \frac{1}{1 - Pa_{22}} + \left(a_{11} - \frac{a_{12}a_{21}}{a_{22}}\right) \frac{-Pa_{22}}{1 - Pa_{22}}.$$
(4)

#### **B.** Proposed Resistance Representation

Relationship (4) has general validity and we can use it to evaluate the resistance  $r_x$  at gate a-b of Fig. 1, by substituting  $x_o$  and  $x_s$  with voltage  $v_x$  and current  $i_x$ , respectively. In particular, referring to gate a-b, relationship (4) allows us to define the *direct resistance*,  $r_{x,ol}$ , (or equivalently the resistance in open-loop configuration) and the *asymptotic resistance*,  $r_{x,\infty}$ , as those resistances evaluated when the control parameter is set to zero and infinity, respectively. Thus, we get

$$r_{x,ol} = \frac{v_x}{i_x}\Big|_{P=0} = a_{11}$$
 (5a)

$$r_{x,\infty} = \left. \frac{v_x}{i_x} \right|_{P \to \infty} = -\frac{a_{12}a_{21}}{a_{22}} + a_{11}.$$
 (5b)

Moreover, it is apparent that once we explicitly identify the controlled source element (represented by the controlled source quantity,  $x_j$ , the controlling quantity,  $x_i$ , and the critical parameter P, so that  $x_j = Px_i$ ), the term  $a_{22}P$  is the corresponding return ratio. It is worth noting, however, that for the return ratio, the input signal ( $x_s$  in Fig. 2) must be set to zero. This corresponds to setting the input source,  $i_x$ , to zero. Thus, the return ratio must be evaluated with the gate a-b open, that is

$$T(\infty) = -P \left. \frac{v_x}{i_x} \right|_{i_x=0} = -P \left. \frac{v_x}{i_x} \right|_{R_x \to \infty} = -Pa_{22}.$$
 (5c)

In conclusion, the resistance  $r_x$  at gate a-b takes the form

$$r_x = r_{x,\infty} \frac{T(\infty)}{1 + T(\infty)} + r_{x,ol} \frac{1}{1 + T(\infty)}$$
(6)

Differently from the Blackman theorem, the novel representation in (6) requires the evaluation of one return ratio only. Moreover, likewise the Rosenstark theorem, it makes use of an *asymptotic term*, given by resistance  $r_{x,\infty}$ , which describes the closed-loop resistance at gate a-b when an ideal and infinite return ratio is assumed. Hence, even if the number of terms involved equals those of the Blackman theorem, the proposed representation introduces a new asymptotic term that has a clear physical meaning and can be more useful to the designer.

## C. Resistance Evaluation

To evaluate the three terms of the closed-loop resistance relationship (4), we have to explicitly identify a critical controlled source (represented by  $x_j = Px_i$ ) and follow the steps given below.

Step 1) Evaluation of the open-loop resistance  $r_{x,ol}$ : Turn off the critical controlled source setting P = 0 and evaluate the resistance of the gate a-b to achieve its open-loop value. Note that, if P is associated with a



Fig. 3. Feedback amplifier example. (a) Transistor level model. (b) Small-signal model.

controlled current (voltage) source, condition P = 0requires an open circuit (short circuit) to replace the branch containing the controlled source.

- Step 2) Evaluation of the asymptotic resistance  $r_{x,\infty}$ : Set the critical parameter to infinity  $(P \rightarrow \infty)$  and evaluate the resistance of the gate a-b to achieve its asymptotic value. Under this condition, since the controlled variable,  $x_j$ , must be a finite quantity, the controlling variable,  $x_i$ , is forced to 0. Note that the asymptotic resistance, stands for the resistance under the ideal condition of infinite loop-gain.
- Step 3) Evaluation of the return ratio  $T(\infty)$ : Maintain the gate a-b open  $(R_{ab} \rightarrow \infty)$  and replace the critical controlled voltage (current) source with an independent voltage (current) generator, S. Then evaluate  $a_{22} = x_i/S$  and the return ratio as  $T(\infty) = -Pa_{22} = -P(x_i/S)$ .

#### IV. EXAMPLE

In order to show the use of relationship (6), consider the circuit in Fig. 3(a), where a common source topology with source degeneration and a further feedback resistance,  $R_F$ , is depicted. Its small-signal model, neglecting MOS output resistance and setting to zero (i.e., shorting) the input signal generator, is plotted in Fig. 3(b).

Assume the voltage-controlled current source as the critical element and let the transconductance  $g_m$  be the control parameter, P. According to Section III, setting P = 0 and inspecting the circuit in Fig. 3(b), it is easy to find the resistance at gate a-b which results

$$r_{x,ol} = (R_S + R_F) || R_L.$$
(7)

To evaluate the asymptotic resistance,  $r_{x,\infty}$ , we first analyze the circuit in Fig. 4, where also the current probe,  $i_{\text{probe}}$ , is included, and then we add the contribution of the resistance  $R_L$ . In the process, we consider the critical parameter infinitely large and, since the controlled variable,  $g_m v_{gs}$ , must be a finite



Fig. 4. Simplified circuit to evaluate the asymptotic resistance.

quantity, this means setting the voltage  $v_{gs}$  equal to zero. Thus, having  $v_g = v_s$ , we can write

$$i_{\rm RF} = \frac{R_E}{R_S} g_m v_{gs} \tag{8}$$

and, considering that  $i_{\text{probe}} = i_{\text{RF}} + g_m v_{gs}$ , we have

$$g_m v_{gs} = \frac{R_S}{R_E + R_S} i_{\text{probe.}}$$
(9)

Therefore, the voltage  $v_{ab}$  at gate a-b is

$$v_{ab} = (R_F + R_S) i_{\rm RF} = \frac{R_F + R_S}{R_E + R_S} R_E i_{\rm probe} \qquad (10)$$

which gives

$$r_{x,\infty,\text{simpl}} = \frac{v_{ab}}{i_{\text{probe}}} = \frac{R_F + R_S}{R_E + R_S} R_E \tag{11}$$

and, considering the contribution of  $R_L$ , the final asymptotic resistance results

$$r_{x,\infty} = \left(\frac{R_F + R_S}{R_E + R_S} R_E\right) ||R_L.$$
 (12)

It is worth noting that (12) returns the ideal zero output resistance when a pure shunt connection is present (i.e.,  $R_E = 0$ ) and returns  $R_L$  in the case of a pure series connection (i.e.,  $R_F \to \infty$ ).

To evaluate the return ratio  $T(\infty)$  we first substitute the controlled source  $g_m v_{gs}$  with an independent current source,  $i_S$ , and then we search for the former controlling voltage,  $v_{gs}$ . Voltage  $v_g$  is given by the amount of current  $i_S$  that flows on  $R_S$  due to the parallel between  $R_S + R_F$  and  $R_L$ , that is

$$v_g = -\frac{R_S R_L}{R_F + R_S + R_L} i_S \tag{13}$$

whereas voltage  $v_s$  is simply given by the product  $R_E i_S$ .

Therefore, we have

$$T(\infty) = -g_m \left. \frac{v_{gs}}{i_S} \right|_{i_x = 0} = g_m \left( \frac{R_S R_L}{R_S + R_F + R_L} + R_E \right).$$
(14)

Finally, using relationships (7), (12), and (14) into (6), we find resistance  $r_x$ , as shown in (15) at the bottom of the next page.



Fig. 5. Indefinite form example. (a) Transistor level model. (b) Small-signal model.

#### V. REMARKS

#### A. Cases With Indefinite Form

The proposed approach seems to be very interesting, since it overcomes the above mentioned drawbacks of the Blackman theorem. However, in some simple cases a difficulty could arise for the determination of an indefinite mathematical form.

For example consider the circuit shown in Fig. 5(a) and suppose we want to calculate the output resistance following the procedure discussed in Section III. The equivalent small-signal model is shown in Fig. 5(b), where also the MOS output resistance is included. The direct resistance can be immediately derived

$$r_{x,ol} = r_o + R_E. \tag{16a}$$

To evaluate the return ratio we note that with the gate a-b open, the current through resistance  $R_E$  is zero thus setting voltage  $v_{as}$  to zero, also. Hence, we have

$$T(\infty) = 0. \tag{16b}$$

Regarding the asymptotic resistance, if  $g_m$  goes to infinity and the product  $g_m v_{gs}$  must remain a finite quantity, then  $v_{gs} = 0$ . As a consequence the current through  $R_E$ , or equivalently the probe current, is forced to be zero. Thus, we get

$$r_{x,\infty} = \infty. \tag{16c}$$

Substituting (16) into (6), the indefinite form " $0 \cdot \infty$ " is apparent.

The situation analyzed is common for some circuits which present a series-feedback connection at the gate of interest. In particular, it occurs when there is no shunt resistance between the two nodes of the gate.

The drawback can be simply overcome by introducing a dummy resistor between nodes a and b,  $R_{ab}$ . Then, we determine the terms  $r_{x,ol}(R_{ab})$ ,  $r_{x,\infty}(R_{ab})$  and  $T(R_{ab})$ , and, after evaluating  $r_x(R_{ab})$  through (6), we have only to calculate the limit for  $R_{ab} \to \infty$ . Referring to the example in Fig. 5, under this assumption (16) becomes

$$r_{x,ol}(R_{ab}) = \frac{(r_o + R_E)R_{ab}}{r_o + R_E + R_{ab}}$$
(17a)

$$r_{x,\infty}(R_{ab}) = R_{ab}$$
(17b)

$$T(R_{ab}) = \frac{g_m r_o R_E}{r_o + R_E + R_{ab}}.$$
 (17c)

Then (6) results in (18), shown at the bottom of the page, where the indefinite form is no more present.

#### **B.** Admittance Complementary Form

Instead of the resistance, we can evaluate the admittance,  $g_x$ , at gate a-b of a feedback circuit through the complementary form of relationship (6). Specifically, we can evaluate (4) for the particular case in which  $x_o$  and  $x_s$  are the current,  $i_x$ , and the voltage,  $v_x$ , at gate a-b, respectively. Therefore, conductance  $g_x$  takes the form

$$g_x = g_{x,ol} \frac{1}{1 + T(0)} + g_{x,\infty} \frac{T(0)}{1 + T(0)}.$$
 (19)

$$r_{x} = \frac{\left[\left(\frac{R_{F} + R_{S}}{R_{E} + R_{S}} R_{E}\right) || R_{L}\right] g_{m} \left(\frac{R_{S} R_{L}}{R_{S} + R_{F} + R_{L}} + R_{E}\right) + (R_{F} + R_{S}) || R_{L}}{1 + g_{m} \left(\frac{R_{S} R_{L}}{R_{S} + R_{F} + R_{L}} + R_{E}\right)}$$
(15)

$$r_{x} = \lim_{R_{ab} \to \infty} \left[ R_{ab} \frac{\frac{g_{m} r_{o} R_{E}}{r_{o} + R_{E} + R_{ab}}}{1 + \frac{g_{m} r_{o} R_{E}}{r_{o} + R_{E} + R_{ab}}} + \frac{(r_{o} + R_{E}) R_{ab}}{r_{o} + R_{E} + R_{ab}} \frac{1}{1 + \frac{g_{m} r_{o} R_{E}}{r_{o} + R_{E} + R_{ab}}} \right] = r_{o} + R_{E} + g_{m} r_{o} R_{E}$$
(18)

In this case, once we explicitly identify the controlled source element,  $g_{x,ol}$  and  $g_{x,\infty}$  are evaluated as

$$g_{x,ol} = \left. \frac{i_x}{v_x} \right|_{P=0} \tag{20a}$$

$$g_{x,\infty} = \left. \frac{i_x}{v_x} \right|_{P \to \infty}.$$
 (20b)

Conductance  $g_{x,ol}$  is the *direct conductance*, while,  $g_{x,\infty}$  is the *asymptotic conductance*. Parameter T(0) takes the general form  $-P[x_i/x_j]_{x_s=0}$ . Note, however, that in this case, setting  $x_s = 0$  means setting  $v_x = 0$ . Hence, T(0) is evaluated when the input source,  $v_x$ , is zero, that is when the gate a-b is short-circuited, namely

$$T(0) = -P \left. \frac{x_i}{x_j} \right|_{v_x=0} = -P \left. \frac{x_i}{x_j} \right|_{R_x=0}.$$
 (20c)

It is worth noting that the use of the complementary form to calculate the admittance of a feedback circuit could represent an alternative way to solve the cases in which relationship (6) has an indefinite form.

# C. Resulting Properties

Once the controlled source element is explicitly defined, a comparison between the definitions of (5a)-(5b) and (20a)-(20b) reveals that

$$r_{x,ol} = \frac{1}{q_{x,ol}} \tag{21a}$$

$$r_{x,\infty} = \frac{1}{g_{x,\infty}}.$$
 (21b)

Moreover, since

$$r_x = \frac{1}{g_x} \tag{22}$$

substituting (5) and (20) into (22) and taking into account (21), allows us to write

$$\frac{r_{x,ol} + r_{x,\infty}T(\infty)}{1 + T(\infty)} = \frac{r_{x,ol}r_{x,\infty}[1 + T(\infty)]}{r_{x,ol}T(o) + r_{x,\infty}}$$
(23)

which, after manipulation, leads to

$$\frac{r_{x,ol}T(0)}{r_{x,\infty}T(\infty)} + \frac{r_{x,\infty}}{r_{x,ol}} = \frac{T(0)}{T(\infty)} + 1.$$
 (24)

Relationship (24) is satisfied if<sup>1</sup>

$$\frac{T(\infty)}{T(0)} = \frac{r_{x,ol}}{r_{x,\infty}} = \frac{g_{x,\infty}}{g_{x,ol}}$$
(25)

which relates the four quantity discussed in the present brief.

<sup>1</sup>Equation (25) is satisfied for  $r_{x,ol} = r_{x,\infty}$ , also. However, this solution is unrealistic.

# VI. CONCLUSION

In this brief, we have proposed an alternative representation and a related methodology for evaluating the exact input and output resistances of feedback amplifiers. It is a generalization of the Rosenstark representation, and, differently from the Blackman approach (that concentrates on one resistance, and on two return ratio terms), our approach focuses on one return ratio, only, and on two resistances (direct and asymptotic) each one evaluated in one of the two ideal and extreme conditions of the return ratio (zero or infinity). Due to this characteristic, the representation allows one to understand what happens to a feedback amplifier resistance in the case of absence of feedback or in the ideal case of infinite feedback.

### REFERENCES

- H. Bode, Network Analysis and Feedback Amplifier Design. New York: van Nostrand, 1945.
- [2] E. Cherry and D. Hopper, Amplifying Devices and Low-Pass Amplifier Design. New York: Wiley, 1968.
- [3] J. Millman and A. Grabel, *Microelectronics*, 2nd ed. New York: Mc-Graw-Hill, 1987.
- [4] A. Sedra and K. Smith, *Microelectronic Circuits*, 3rd ed. Philadelphia, PA: Saunders, 1991.
- [5] P. Gray, P. Hurst, S. Lewis, and R. Meyer, Analysis and Design of Analog Integrated Circuits, 4th ed. New York: Wiley, 2001.
- [6] K. Laker and W. Sansen, Design of Analog Integrated Circuits and Systems. New York: McGraw-Hill, 1994.
- [7] D. Johns and K. Martin, Analog Integrated Circuit Design. New York: Wiley, 1997.
- [8] G. Palumbo and J. Choma Jr., "An overview of analog feedback Part I: Basic theory," *Anal. Integr. Circuits Signal Process.*, vol. 17, no. 3, pp. 175–194, Nov. 1998.
- [9] —, "An overview of analog feedback Part II: Amplifier configurations in generic device technologies," *Anal. Integr. Circuits Signal Process.*, vol. 17, no. 3, pp. 195–219, Nov. 1998.
- [10] G. Palumbo and S. Pennisi, Feedback Amplifiers (Theory and Design). Reading, MA: Kluwer, 2002.
- [11] J. Marrero, "Simplified analysis of feedback amplifiers," *IEEE Trans. Educ.*, vol. 48, no. 1, pp. 53–59, Feb. 2005.
- [12] J. Ramirez-Angulo, R. G. Carvajal, and A. Torralba, "Low supply voltage high-performance CMOS current mirror with low input and output voltage requirements," *IEEE Trans. Circuits Syst. II, Exp. Briefs*, vol. 51, no. 3, pp. 124–129, Mar. 2004.
- [13] "A regulated body-driven CMOS current mirror for low-voltage applications," *IEEE Trans. Circuits Syst. II, Exp. Briefs*, vol. 51, no. 10, pp. 571–577, Oct. 2004.
- [14] S. M. Potirakis and G. E. Alexakis, "An accurate calculation of Miller effect on the frequency response and on the input and output impedance of feedback amplifiers," *IEEE Trans. Circuits Syst. II, Exp. Briefs*, vol. 52, no. 8, pp. 491–495, Aug. 2005.
- [15] E. Cherry, "Input impedance and output impedance of feedback amplifiers," *IEE Proc. Circuits Devices Syst.*, vol. 143, no. 4, pp. 195–201, Aug. 1996.
- [16] R. Blackman, "Effect of feedback on impedance," BSTJ, vol. 22, pp. 269–277, Oct. 1943.
- [17] S. Rosenstark, "A simplified method of feedback amplifier analysis," *IEEE Trans. Educ.*, vol. E-17, no. 4, pp. 192–198, Nov. 1974.
- [18] J. Choma Jr., "Signal flow analysis of feedback networks," *IEEE Trans. Circuits Syst.*, vol. 37, no. 4, pp. 455–463, Apr. 1990.